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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Harumitsu Fujita Date: June 4, 2001

Serial No.: Not Yet Assigned.

Filed: June 4, 2001 (Herewith)

For: MULTI-VOLTAGE LEVEL SEMICONDUCTOR
DEVICE AND ITS MANUFACTURE

jc978 U.S. PTO
09/073580
06/04/01

Assistant Commissioner for Patents
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT ACCOMPANYING
REQUEST FOR DIVISIONAL APPLICATION UNDER 37 C.F.R. §1.53(b)**

Sir:

Submitted herewith is a form PTO-1449 listing references for consideration by the Examiner. Applicants respectfully request the listed references be made of record in the subject application.

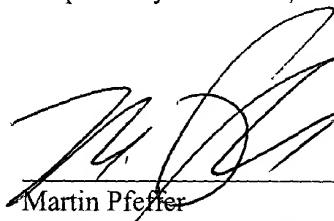
In accordance with 37 C.F.R. §1.98(d), copies of any of the listed references which were previously cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 09/021,519 are not included with this submission.

No fee is deemed necessary in connection with the filing of the subject Information Disclosure Statement. However, in the event a fee is required or if any additional fee due during the pendency of this application is not paid, the Patent and Trademark Office is authorized to charge the underpayment to Deposit Account No. 15-0700.

EXPRESS MAIL CERTIFICATE

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee (mail label #EL583738104US) in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on June 4, 2001:

Respectfully submitted,



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